

PATENT NUMBER

JCI  
JC645

O.I.P.E. SCANNED 153	PATENT DATE
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APPLICATION NO. 09/614286	CONT/PRIOR F	CLASS 438	SUBCLASS 799	ART UNIT 2812	EXAMINER Krelin
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[illegible]

Hiroshi Tanabe

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Method for forming semiconductor films at desired positions on a substrate

PTO-2040  
1289[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print-Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____	_____ (Assistant Examiner)      (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)      (Date)			<b>ISSUE FEE</b>	
				<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      (Date)			<b>ISSUE BATCH NUMBER</b>	

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